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STATEMENT BY APPLICANT

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Complete If Known

Application Number	09/669,730	RECEIVED
Filing Date	09/26/2000	AUG 06 2004
First Named Inventor	FRAMPTON E ELLIS	Technology Center 2100
Art Unit	2152	
Examiner Name		

Attorney Docket Number 081498-0265678

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>D</i>	A	NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, (October 28, 1996).	
<i>D</i>	B	LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992).	
<i>D</i>	C	DICKINSON ET AL, "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, (October 2-4, 1989).	
<i>D</i>	D	NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 (No. 6), p. 1031-1040, (June 6, 1995).	
<i>D</i>	E	ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, (October 23-24, 1995).	
<i>D</i>	F	WU ET AL, "Microprocessor Control Signal Transmission through Optical Fiber," Conference Record of 1992 IEEE Industry Applications Society Annual Meeting, p. 1747-1750, (October 4-9, 1992).	

Examiner Signature	<i>D, DINM</i>	Date Considered	9/22/05
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Atty. Dkt. No.	Mr. [REDACTED]	Client Ref. [REDACTED]
265678		GNC7-CON1
Applicant: ELLIS, III		
Appln. No.: Cont. of Appln. No. 09/320,660		
Filing Date: September 26, 2000		
Examiner: Unknown		Group Art Unit: Unknown

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**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Date: September 26, 2000

Page 1 of 5

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
AR	5,381,534	01/1995	Shi	—	—	
BR	5,392,400	02/1995	Berkowitz et al.	—	—	
CR	5,426,741	06/1995	Butts, Jr. et al.	—	—	
DR	5,550,984	08/1996	Gelb	—	—	
ER	5,615,127	03/1997	Beatty et al.	—	—	
FR	5,666,484	09/1997	Orimo et al.	—	—	
GR	5,761,507	06/1998	Govett	—	—	
HR						
IR						
JR						

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	Document Number	Date MM/YYYY	Country	Inventor Name	Class	SubClass	English Abstract		Translation Readily Available	
							Enclosed	No	Enclose	No
DJ	KR EP 0 840216A2	05/1998	Europe	Chung et al.	—	—				
LR										
MR										
NR										
OR										
PR										
QR										
RR										
SR										
TR										

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UR	Litzkow, et al., "Condor-A Hunter of Idle Workstations", 1988 IEEE, pp. 104-111
VR	Theimer, et al., "Finding Idle Machines in a Workstation-Based Distributed System", IEEE Transactions on Software Engineering, November, 1989, Vol. 15, No. 11, pp. 1444-1458
WR	Brown et al., Special Edition Using Netscape™ 2 Second Edition, Que Corporation, 1995, Ch. 1-2
XR	Gilder, "Angst and Awe on the Internet by George Gilder", Forbes ASAP, December 4, 1995
YR	Tandlary, et al., "Batrun: Utilizing Idle Workstations for Large-Scale Computing", Summer 1996, pp. 41-48
ZR	Brisbin, "Shopping for Internet access", MacUser, December, 1994, v.10, p. 133(2)

Examiner *O D/WH* Date Considered: *9/26/00*

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Date: September 26, 2000 Page **2** of **5**

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OTHER (Including in this order: Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

<input checked="" type="checkbox"/>	UR	Gilder, "The Bandwidth Tidal Wave", Forbes ASAP, December 5, 1994
<input checked="" type="checkbox"/>	VR	N/A, "Special Report - Wonder Chips", Business Week, July 4, 1994
<input checked="" type="checkbox"/>	WR	N/A, "Supercomputers Divide and conquer", The Economist, December 14, 1996
<input checked="" type="checkbox"/>	XR	N/A, "Cyber View World Wide Widgets", Scientific American, May, 1997, p. 48
<input checked="" type="checkbox"/>	YR	Gibbs, "Bandwidth, Unlimited", Scientific American, January, 1997, p. 41
<input checked="" type="checkbox"/>	ZR	Markoff, "A New Standard to Govern PC's With Multiple Chips, Work Stations Mimicking Supercomputers", The New York Times, October 28, 1997

Examiner *D. Dinsl* Date Considered: *9/21/00*

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Atty. Dkt. No.	My 265678	Client Ref. GNC7-CONA
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Date: September 26, 2000 Page **3** of **5**

Applicant: ELLIS, III
 Appln. No.: Cont. of Appln. No. 09/320,660
 Filing Date: September 26, 2000
 Examiner: Unknown Group Art Unit: Unknown
 C 922 09/66973

U.S. PATENT DOCUMENTS

Examiner's initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
AR	5,889,989	03/1999	Robertazzi	—	—	
BR	5,260,943	11/1993	Comroe et al.	—	—	
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QR										
RR										
SR										
TR										

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VR	Waldspurger et al., "Spawn: A Distributed Computational Economy", Feb. 1992 IEEE, pp. 103-117
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XR	
YR	
ZR	

D. D. NY

Date Considered:

7/1/00

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Atty. Dkt. No.	M#	Client Ref.
265678		GNC7-CON10

Applicant: ELLIS, III

Appln. No.: Cont. of Appln. No. 09/320,660

Filing Date: September 26, 2000

Examiner: Unknown Group Art Unit: Unknown

Date: September 26, 2000 Page 4 of 5

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AS	AR 5,828,833	10/1998	BELVILLE et al.	395	187.01	08/15/1996
BR	BR 5,826,014	10/1998	COLEY et al.	395	187.01	02/06/1996
CR	CR 5,291,494	03/1994	BRUCKERT et al.	371	11.3	11/18/1992
DR	DR 5,845,074	12/1998	KOBATA	—	—	
ER	ER 5,680,548	10/1997	TRUGMAN	—	—	
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HR						
IR						
JR						

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KR										
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SR										
TR										

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UR	Hare et al., "Master the Complexities of Network Security", <u>Internet Firewalls and Network Security</u> , Second Edition, pp. 325-350 and 516		
VR	Weiyi et al., "Java-to-Go - Iterative Computing Using Java," September 10, 1996 http://ptolemy.eecs.berkeley.edu/dgm/javatools/java-to-go/		
WR	Sullivan et al., "A New Major SETI Project Based on Project Serendip Data and 100,000 Personal Computers." http://setiathome.ss.berkeley.edu/woody_paper.htm/		
XR			
YR			

Examiner D. Dunn Date Considered: 9/26/00

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265678		GNC7-COM1
Applicant: ELLIS, III		
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Filing Date: September 26, 2000		
Examiner: D. Dinh		Group Art Unit: Unknown

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Page

5 of 5

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Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)		Class	Sub Class	Filing Date (if appropriate)
✓ AR	5,864,738	01/1999	Kessler et al.		—	—	
✓ BR	5,793,968	08/1998	Gregerson et al.		—	—	
✓ CR	5,909,681	06/1999	Passera et al.		—	—	
✓ DR	5,572,643	11/1996	Judson		—	—	
✓ ER	5,590,284	12/1996	Crosetto		—	—	
✓ FR	5,930,511	07/1999	Hinsley		—	—	
✓ GR	5,978,829	11/1999	Chung et al.		—	—	
✓ HR	5,748,489	05/1998	Beatty et al.		—	—	
✓ IR	5,696,902	12/1997	Leclercq et al.		—	—	
✓ JR	5,349,682	09/1994	Rosenberry		—	—	
✓ KR							
✓ LR							

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	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract		Translation Readily Available	
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✓ MR	0 647 052 A1	09/1994	European	Leclercq, Thierry				
NR								

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✓ OR	Geoffrey C. Fox and Wojtek Furmanski, Petaops and Exaops: Supercomputing on the Web, "IEEE INTERNET COMPUTING," Vol. 1, Number 2 March-April 1997, pgs. 38-46.			
✓ PR	Kivanc Dincer and Geoffrey C. Fox, Building a World-Wide Virtual Machine Based on Web and HPCC Technologies, "STUDENT TECHNICAL PAPERS," http://www.supercomp.org/sc96/proceedings/SC96PROC/DINCER/INDEX.HTM pgs. 1-18.			
✓ QR	M. Hobbs and A. Goscinski, A Remote Porcess Creation and Execution Facility Supporting Parallel Execution on Distributed Systems, "IEEE 1996," pgs. 92-99.			
✓ RR	Taisuke Boku, Hiroshi Nakamura, Kisaburo Nakazawa, and Yoichi Iwasaki, The Architecture of Massively Parallel Processor CP-PACS, "IEEE 1997," pgs. 31-40.			
✓ SR	Yoon-Hwa Choi and Yu-Seok Kim, A Diagnostic Network for Massively Parallel Processing Systems, "IEEE 1994," pgs. 348-353.			
✓ TR	Steven M. Bellovin and William R. Cheswick, Network Firewalls, "IEEE Communications Magazine 1994," pgs. 50-57.			
UR				
VR				

Examiner

b. dmt

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